

Two Day National Level Faculty Development Programme

on

Digital Signal Processing

13th & 14th December 2019

Registration Form

Name:

Designation:

Department:

Institution:

Mailing address:

Phone No.:

Email id:

Accommodation Required:

Declaration

The above information is true to the best of my knowledge. I agree to abide by the rules and regulations governed by the faculty development programme. If I am selected, I shall attend the course for entire duration.

Signature of Applicant

Signature of HOD

Address for Communication

Department of ECE,
School of Engineering,

Avinashilingam Institute for Home Science and Higher
Education for Women,
Coimbatore - 641108

Tel: 0422-2658145/2988997/2658716

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Email: hodeceau1@gmail.com

ORGANIZERS

Chief Patron Dr. T. S. K. Meenakshisundaram
Managing Trustee

Patrons Dr. P.R. Krishnakumar
Chancellor

Dr. Premavathy Vijayan
Vice- Chancellor

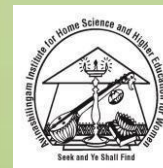
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Registrar

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Dean, School of Engineering

Organizing Secretary Dr. B. Sargunam
Associate Professor & HoD
Department of Electronics and
Communication Engineering

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Mrs. G. Mahalakshmi Malini, AP/ECE
Ms. K.R. Priyanga, AP/ECE
Mrs. P.G. Padmagowri, AP/EEE
Mrs. M. Aghalya, AP/ECE



Avinashilingam

Institute for Home Science and Higher
Education for Women

(Deemed to be University under Category 'A'
by MHRD, Estd.u/s 3 of UGC Act 1956)

Re-accredited with 'A+' Grade by NAAC,
Recognised by UGC under Section 12B
Coimbatore - 641043, TamilNadu, India

Two Day National Level Faculty Development Programme on Digital Signal Processing



(13th & 14th December 2019)

Jointly Organized by
Department of Electronics and
Communication Engineering
and

IETE Students' Forum

School of Engineering

(Approved by AICTE)

Avinashilingam Institute for Home Science and
Higher Education for Women, Coimbatore-641 108

Website: www.avinuty.ac.in

About the Institute

Sri Avinashilingam Home Science College for Women was established under the auspices of the Avinashilingam Education Trust in 1957 by Dr. T.S. Avinashilingam, the great Patriot and educationist, the first Minister of Education in the Composite State of Madras (1946 - 1949) and a Padma Bhushan Awardee. World renowned Doyen of Nutrition and the first Vice Chancellor, Padmashri Dr. Rajammal P. Devadas, nurtured and raised this institution to its present status as a University, with her ardent and unstinted efforts. The University is scaling global heights under the stewardship of Dr. T.S.K. Meenakshisundaram, Managing Trustee, Padmashri Dr.P.R.Krishnakumar, Chancellor, Dr. Premavathy Vijayan, Vice Chancellor and Dr. S. Kowsalya, Registrar of the Institute. The tremendous growth as an autonomous college was visible on ten years from 1978 in various spheres of academics, community development and skill development of women, the society and in National and International research and consultancy. These fast paced contributions aided the UGC and MHRD in recognizing the institution as a Deemed to be University in 1988. Thus, the institution has been in the limelight both at National and International levels since then. The institute has diversified and well-qualified faculty. Around 7000 students benefit from the Undergraduate, Postgraduate and Research level programmes offered by the School of Home Science, Physical Sciences and Computational Sciences, Bio Sciences, Arts and Social Sciences, Commerce and Management, Education and Engineering.

School of Engineering

The School of Engineering was started in the year 1996 with the approval of AICTE. The campus is spread over an area of 100 acres and is situated 6 km off Mettupalayam Road at Thudiyalur. The campus is well developed with excellent infrastructure for classrooms, laboratories, library, hostel, on-campus medical and banking facilities, transportation, telecom, stationery, cafeteria, 24 hour internet facility and Wi-Fi connectivity.

About the Department

The Department of Electronics and Communication Engineering is one of the most versatile and fast developing Branches of Engineering, established in the year 2000. The curriculum has been framed and updated regularly to keep up with the growing demands of the research community and in the design and automation sectors of industries.

The department offers a value added course on 'Electronics for all'. The department has well established laboratories like Electronics Laboratory, IC and Communication Laboratory, Microwave and Fibre Optic Laboratory, VLSI Design Laboratory, Digital Signal Processing Laboratory and Embedded Systems Laboratory. IETE students' forum, a professional body, is coordinated by the department.

VLSI Design is an emerging field of specialization with its own demand in industry and research. To meet such a demand, the Department of Electronics and Communication Engineering offers Master of Engineering in VLSI Design. The goal of this programme is to equip students with wide practical knowledge in the field of VLSI design and testing.

Also, the Department of ECE offers Ph.D programme in Electronics and Communication Engineering.

About the FDP

Signal processing is concerned with the representation, manipulation and transformation of signals and the information that they carry. It provides techniques for processing acquired time-series data for the purpose of efficient analysis and synthesis. Over the years, it has been developed to give great advancements in the areas of microchips, digital systems and computer hardware. Hence, it is necessary to make teachers of engineering colleges and Universities and industry aware of the opportunities and

challenges in this field. This programme will also serve as an induction programme for Ph.D researchers working in the area of signal processing

Brief list of topics to be covered

- Introduction to Signals & Systems, DFT and FFT
- Design of IIR and FIR filters
- Finite world length effects
- Multi-rate signal processing applications
- DSP processors

Resource Persons

Dr. Nagarajan Malmurugan, Principal,
Mahendra College of Engineering, Salem
Dr. P.Subbaraj, Dean Research (Electrical Sciences)
PSG Institute of Technology and Applied Research
Coimbatore.
Dr. S. Esakkirajan Prof. ICE PSGCT
Dr. V.R.Vijay Kumar, Associate Professor, ECE, Anna
University Regional Campus, Coimbatore
Dr. B.Nataraj, Associate Professor, ECE, SREC,
Coimbatore
Dr. R.Sudarmani, Asso. Prof, ECE, School of Engineering,
Avinashilingam Institute, Coimbatore
Mrs. R.Chitra, Assistant Professor (SS), ECE, School of
Engineering, Avinashilingam Institute, Coimbatore

Eligibility: Faculty Members and Research Scholars Participants

Number of participants is limited to 30 on first come first serve basis. Filled in application form should be sent to coordinators through mail.

Registration amount: Rs.500/-.

Lunch & refreshments shall be provided.

Important Dates:

Last date for receipt of filled in application form: **05.12.2019**

Intimation of selection through E-mail: **08.12.2019**